Amendments to the Specification

Please replace the paragraph [0009] at page 3, line 23 to page 5, line 14 with the following amended paragraph:

[0009] The present invention is directed to a portable electronic device with power failure recovery, and a method for operating the same. The portable electronic device is powered by a main power source, which comprises a power detection module, a processor, a timing unit and a power management unit. The power detection module detects an output characteristic from the main power source, so as to assert an interrupt signal if the detected output characteristic is below a first threshold value. In response to the interrupt signal, the processor asserts a turn-off signal and an enable signal. The timing unit, responsive to the enable signal, asserts a notification signal at a predetermined time interval when the enable signal is asserted, in which the timing unit is directly powered by a backup power source. The power management unit is electrically coupled to the main power source and the backup power source. The power management unit disconnects the main power source ~~to~~ from a circuit block ~~with high power consumption~~ when the turnoff signal is asserted. Additionally, the power management unit reconnects the main power source to the circuit block ~~with high power consumption~~ when the notification signal is asserted and the output characteristic of the main power source is beyond a second threshold value. Another aspect of the invention is to disclose a portable electronic device with power failure recovery including a power detection module, a volatile RAM, a processor, a timing unit and a power management unit. The portable electronic device is powered by a main power source.

The power detection module detects an output characteristic from the main power source to assert an interrupt signal when a power failure in the main power source occurs and the detected output characteristic is below a first threshold value. The volatile RAM stores operation data when the power failure in the main power source occurs. The processor is adapted to respond to the interrupt signal, and asserts a turn-off signal and an enable signal. In response to the enable signal, the timing unit asserts a notification signal at a predetermined time interval when the enable signal is asserted. The power management unit is electrically coupled to the main power source and a backup power source. The power management unit supplies the timing unit and the volatile RAM with electrical power from the backup power source. In addition, the power management unit disconnects the main power source ~~to~~ from a circuit block ~~with high power consumption~~ when the turn-off signal is asserted. The power management unit reconnects the main power source to the circuit block ~~with high power consumption~~ when the notification signal is asserted and the output characteristic of the main power source is beyond a second threshold value. Further, the processor performs a resume operation based on the operation data stored in the volatile memory when main power is resumed.